

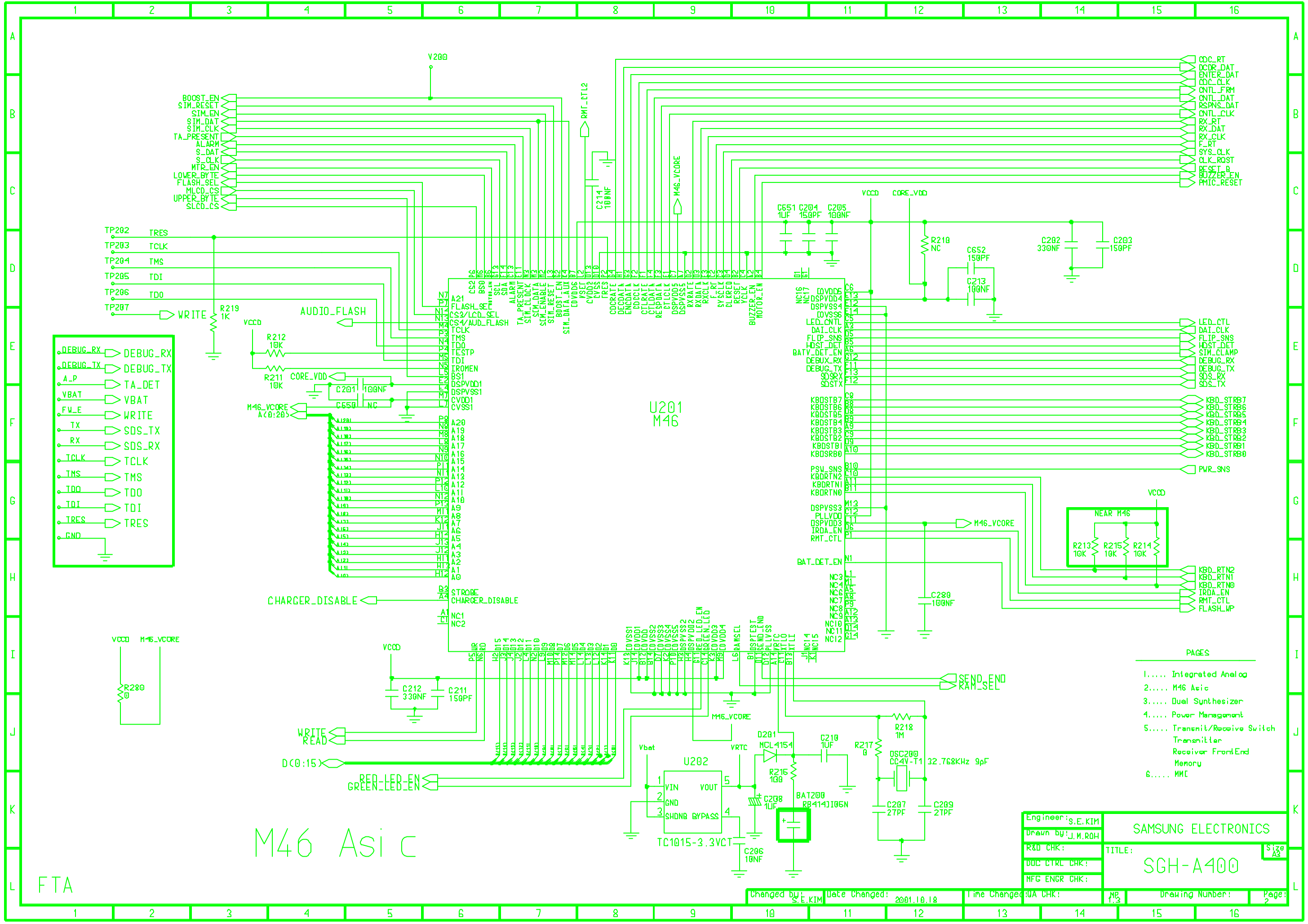
Integrated Analog

PAGES

1.....	Integrated Analog
2.....	M16 Asic
3.....	Dual Synthesizer
4.....	Power Management
5.....	Transmit/Receive Switch
	Transmitter
	Receiver FrontEnd
	Memory
6.....	M16

Engineer: S.E.KIM	SAMSUNG ELECTRONICS				
Drawn by: J.M.ROH					
R&D CHK:					
DOC CTRL CHK:					
MFG ENGR CHK:					
TITLE: SGH-A400					
Changed by: S.E.KIM	Date Changed: 2001.10.18	Time Changed: 09:44	MP: 1/3	Drawing Number:	Page: 1

FTA

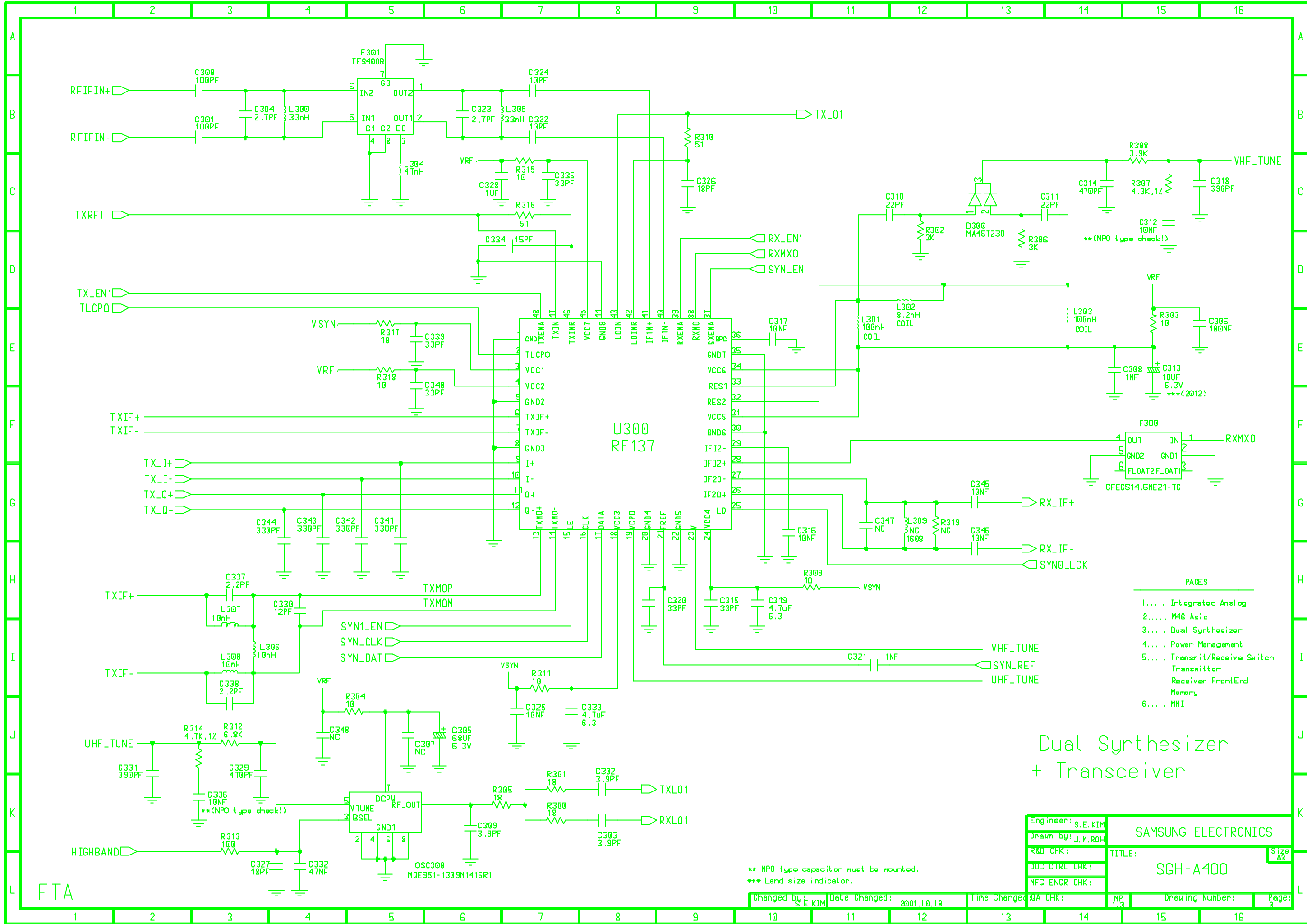


M46 Asi c

FTA

- PAGES
- 1..... Integrated Analog
 - 2..... M46 Asi c
 - 3..... Dual Synthesizer
 - 4..... Power Management
 - 5..... Transmit/Receive Switch
Transmitter
Receiver FrontEnd
Memory
 - 6..... MMC

Engineer: S.E.KIM	SAMSUNG ELECTRONICS
Drawn by: J.M.ROH	
R&D CHK:	TITLE:
DOC CTRL CHK:	SGH-A400
MFG ENGR CHK:	Size A3



PAGES	
1.....	Integrated Analog
2.....	M4S Asic
3.....	Dual Synthesizer
4.....	Power Management
5.....	Transmil/Receive Switch Transmitter Receiver FrontEnd Memory
6.....	MMI

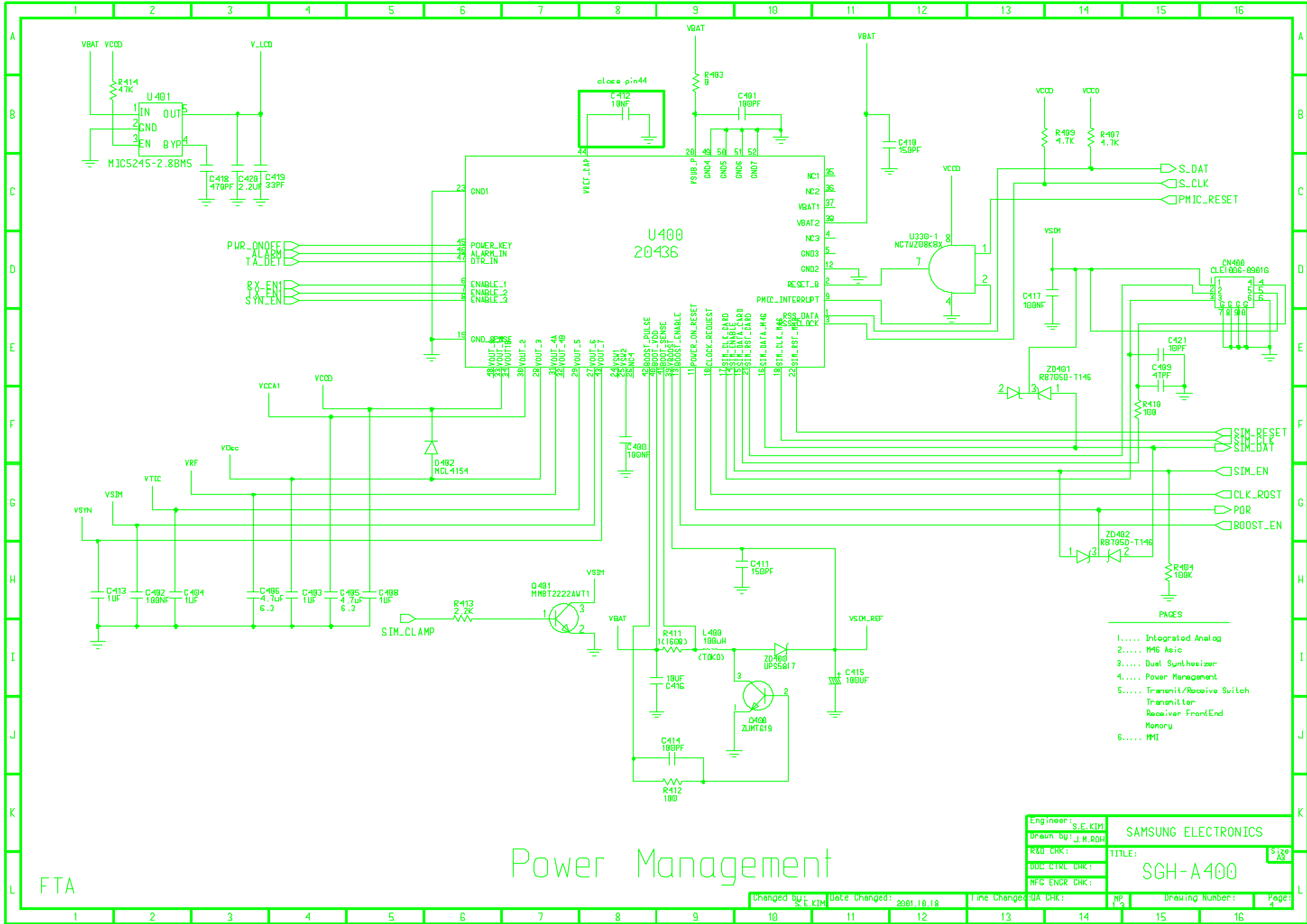
Dual Synthesizer + Transceiver

Engineer: S.E.KIM	SAMSUNG ELECTRONICS	
Drawn by: J.M.ROH	TITLE:	Size: A3
R&D CHK:	SGH-A400	
DOC CTRL CHK:		
NFG ENGR CHK:		

** NPO type capacitor must be mounted.
*** Land size indicator.

Changed by: S.E.KIM	Date Changed: 2001.10.18	Time Changed: 09:44	QA CHK:	NP: 1/3	Drawing Number:	Page: 3
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FTA

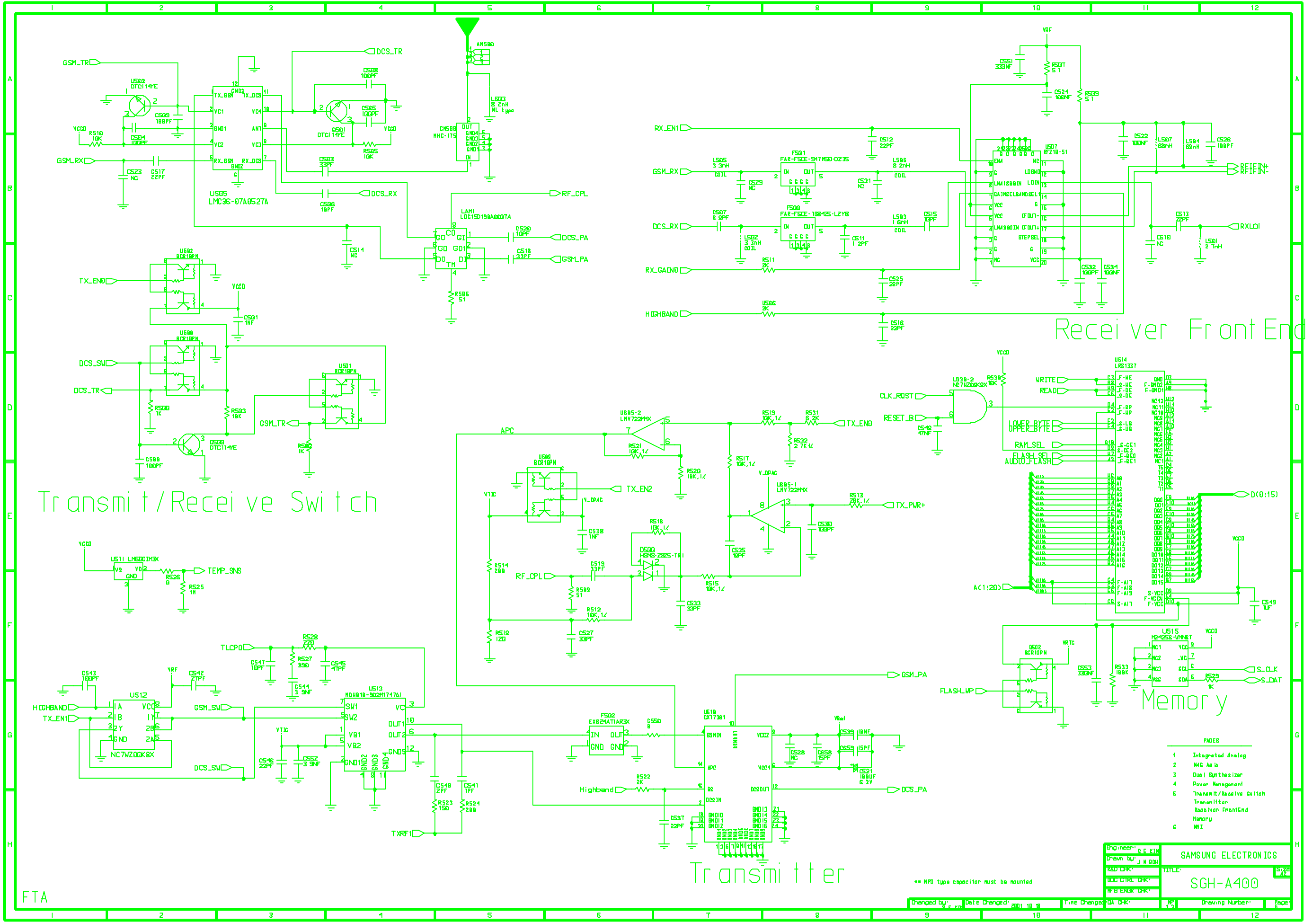


Power Management

FTA

- PAGES
- 1..... Integrated Analog
 - 2..... M46 Asic
 - 3..... Dual Synthesizer
 - 4..... Power Management
 - 5..... Transmit/Receive Switch
Transmitter
Receiver FrontEnd
Memory
 - 6..... MMI

Engineer: S.E.KIM	SAMSUNG ELECTRONICS
Drawn by: J.M.ROH	
R&D CHK:	TITLE:
DOC CTRL CHK:	SGH-A400
NFG ENGR CHK:	Size A3
QA CHK:	MP 1/2
Changed by: S.E.KIM	Date Changed: 2001.10.18
Time Changed:	Drawing Number:
Page:	Page:



Receiver Front End

Transmit / Receive Switch

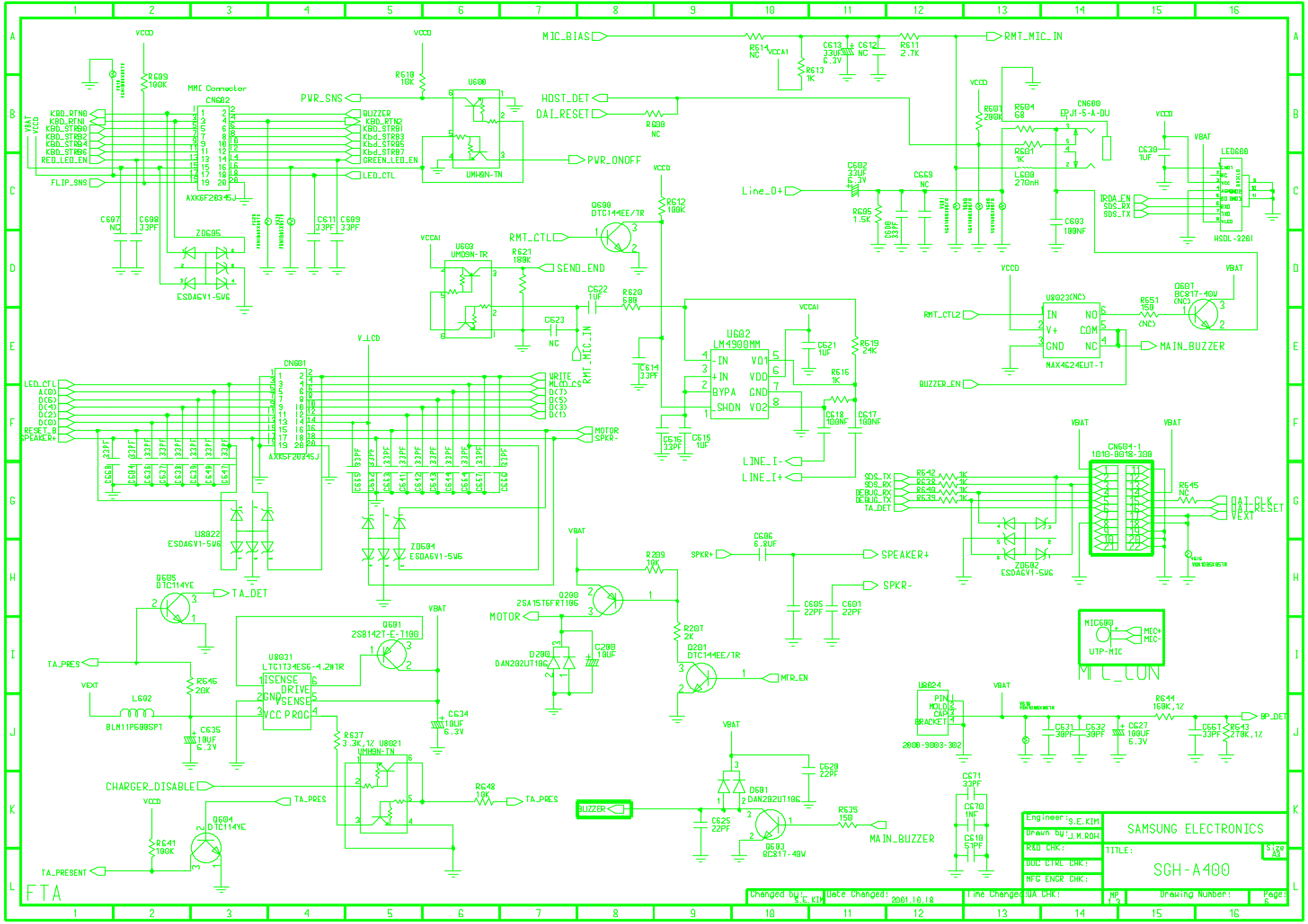
Memory

Transmitter

- PADES
- 1 Integrated Analog
 - 2 MAC Axi
 - 3 Dual Synthesizer
 - 4 Power Management
 - 5 Transmit/Receive Switch
 - 6 Transmitter
 - 7 Receiver FrontEnd
 - 8 Memory
 - 9 NMI

Eng. Name: J. E. Kim	SAMSUNG ELECTRONICS
Drawn by: J. M. Choi	TITLE: SGH-A400
Rev. Date: 2001.10.18	DATE: 2001.10.18
Checked by: J. M. Choi	DATE: 2001.10.18
Drawn Number: 1000000000	Page: 1/1

** NPD type capacitor must be mounted



Engineer: S.E. KIM	SAMSUNG ELECTRONICS	
Drawn by: J.M. ROH	TITLE:	Size: A3
R&D CHK:	SGH-A400	
DOC CTRL CHK:	MP:	Drawing Number:
NFG ENGR CHK:	1/2	Page:

Changed by: S.E. KIM Date Changed: 2001.10.18 Time Changed: 09:44 CHK: NP

FTA